|  | •   |  |  | OMMERCE PATENT AND TRADEMARK OFFICE                                 | ATTORNEY'S DOCKET NUMBER<br>12816-008001  |  |  |  |  |
|--|---|--|--|---|---|--|--|--|--|
|  |   | C  | ESIGNATED/ELECTED ONCERNING A FILING                             | O OFFICE (DO/EO/US)<br>UNDER 35 U.S.C. 371                          | U.S. APPLICATION NO. (If Known, see 37 CFR 1.5) 09/806140   |  |  |  |  |
|  |   |  | ATIONAL APPLICATION NO.<br>99/02752                              | INTERNATIONAL FILING DATE 1 September 1999                          | PRIORITY DATE CLAIMED 28 September 1998   |  |  |  |  |
| ſ  |   | TITLE OF INVENTION DIGITAL RECEIVER FOR A SIGNAL PRODUCED  |  |   |   |  |  |  |  |
| ı  | APPLICANT(S) FOR DO/EO/US Heinrich Schenk   |  |  |   |   |  |  |  |  |
| ŀ  |   |  |  |   |   |  |  |  |  |
|  | 7.  | Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:  1.   This is a FIRST submission of items concerning a filing under 35 U.S.C. 371.   |  |   |   |  |  |  |  |
|  | 2.  |  |  |   |   |  |  |  |  |
|  | 3.  |  |  |   |   |  |  |  |  |
|  | 4.  |  |  | expiration of 19 months from the priority                           | · · · · · · · · · · · · · · · · · · ·   |  |  |  |  |
|  | <ul> <li>5.  A copy of the International Application as filed (35 U.S.C. 371(c)(2))</li> <li>a.  is attached hereto (required only if not communicated by the International Bureau).</li> <li>b.  has been communicated by the International Bureau.</li> <li>c.  is not required, as the application was filed in the United States Receiving Office (RO/US).</li> </ul> |  |  |   |   |  |  |  |  |
|  | 6.  | $\boxtimes$  | An English language translation of                               | on of the International Application as filed (35 U.S.C. 371(c)(2)). |   |  |  |  |  |
|  | 7.  | <ul> <li>Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))</li> <li>a.  are attached hereto (required only if not communicated by the International Bureau).</li> <li>b.  have been communicated by the International Bureau.</li> <li>c.  have not been made; however, the time limit for making such amendments has NOT expired.</li> <li>d.  have not been made and will not be made.</li> </ul> |  |   |   |  |  |  |  |
| 2000<br>2000<br>2000<br>2000<br>2000<br>2000 | 8.  | 8. An English language translation of amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).   |  |   |   |  |  |  |  |
|  | 9.  |  | An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)). |   |   |  |  |  |  |
|  | 10. An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).  |  |  |   |   |  |  |  |  |
| þá   | Items 11 to 16 below concern other documents or information included:   |  |  |   |   |  |  |  |  |
| 1  | 11.   |  | An Information Disclosure Statem                                 | ent under 37 CFR 1.97 and 1.98.                                     |   |  |  |  |  |
|  | 12.   |  | An assignment document for recoincluded.                         | ording. A separate cover sheet in compl                             | iance with 37 CFR 3.28 and 3.31 is  |  |  |  |  |
| - {  | 13.   | $\boxtimes$  | A FIRST preliminary amendment.                                   |   |   |  |  |  |  |
| ١  |   |  | A SECOND or SUBSEQUENT pro                                       | eliminary amendment.  |   |  |  |  |  |
|  | 14.   | 14. 🔀 A substitute specification.  |  |   |   |  |  |  |  |
|  | 15.   | 15. A change of power of attorney and/or address letter.   |  |   |   |  |  |  |  |
|  | 16.  Other items or information:  |  |  |   |   |  |  |  |  |
|  |   |  |  |   | Express Mail Label No <u>FL258935033US</u> Seeing deposited with the United States Postal Service as Express Mail dicated below and is addressed to the Commissioner for Patents, |  |  |  |  |
|  | !   |  |  | 3-27-2001 Sugnature   | Mantha Bell Sanar Ha Be   |  |  |  |  |

i

|   | O.S. AFFEICATION NO. (IF R   | 72140  | PCT/DE99/02752  | CATION NO.                | ATTORNEY'S DOCK<br>12816-008001 | ET NUMBER |
|---|--|--|---|---------------------------|---------------------------------|-----------|
|   | 17. X The following fee  | es are submitted:  | T GTIDEO0102102   |                           | CALCULATIONS                    | PTOLICE   |
|   | Basic National Fee ( 37 CFR 1.492(a)(1)- (5) ):  |  |   |                           | ONLY                            | PTO USE   |
|   | Neither international prel   |  |   |                           |                                 |           |
|   | nor international search t   |  |   |                           |                                 |           |
|   | and International Search Report not prepared by the EPO or JPO\$1000   |  |   |                           |                                 |           |
|   | International preliminary examination fee (37 CFR 1.482) not paid to   |  |   |                           |                                 |           |
|   | USPTO but International Search Report prepared by the EPO or JPO \$860   |  |   |                           |                                 |           |
|   |  |  |   |                           |                                 |           |
|   | International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO |  |   |                           |                                 |           |
|   |  | ternational preliminary examination fee paid to USPTO (37 CFR 1.482) |   |                           |                                 |           |
|   | but all claims did not sat   |  |   |                           |                                 |           |
|   |  |  |   |                           |                                 |           |
|   | International preliminary  | examination fee pai  | d to USPTO (37 CFR                                      | 1.482)                    |                                 |           |
|   | and all claims satisfied p   |  |   |                           |                                 |           |
|   |  | ENTER A  | PPROPRIATE BAS  | SIC FEE AMOUNT =          | \$860.00                        |           |
| 1   | Surcharge of \$130 for fur   | nishing the oath or  | declaration later than i                                | 20 30                     |                                 |           |
| 1   | months from the earliest   | claimed priority date  | e (37 CFR 1.492(e)).                                    |                           | \$0.00                          |           |
| 120   | Claims   | Number Filed   | Number Extra  | Rate                      |                                 |           |
| 100   | Total Claims   | 7 - 20 =   | 0   | x \$18                    | \$0.00                          |           |
|   | Independent Claims   | 1 - 3 =  | 0   | x \$80                    | \$0.00                          |           |
|   | MULTIPLE DEPENDENT   |  |   | + \$270                   | \$0.00                          |           |
| La.   | Applicant claims small   | Lantitu atatus Cas   | TOTAL OF ABOVE  | CALCULATIONS =            | \$860.00                        |           |
| in<br>Section   | Applicant claims small reduced by 1/2.   | renuty status, see .   | 37 GFR 1.27. The fee                                    | s indicated above are     | <b>#0.00</b>                    |           |
|   |  |  |   | SUBTOTAL =                | \$0.00<br>\$860.00              |           |
| 34  | Processing fee of \$130 for furnishing the English Translation later than ☐ 20 ☐ 30  |  |   |                           | Ψ000.00                         |           |
| 1   | months from the earliest of  | claimed priority date  | (37 CFR 1.492(f))                                       |                           | \$0.00                          |           |
|   | Foo for many district  |  |   | L NATIONAL FEE =          | \$860.00                        |           |
| irant<br>i  | Fee for recording the enclaceompanied by an appro  | losed assignment (;<br>opriate cover sheet                           | 37 CFR 1.21(h)).  The<br>(37 CFR 3.28, 3.31) <b>¢</b> . | assignment must be        | <b>#0.00</b>                    |           |
| Ì   |  | princts so to to cities  |   | FEES ENCLOSED =           | \$0.00<br>\$860.00              |           |
| ı   | TOTAL PEES ENCLUSED =  |  |   |                           |                                 | <b>.</b>  |
| ı   |  |  |   |                           | Amount to be<br>refunded:       | \$        |
|   |  |  |   |                           | Charged:                        | \$        |
| 1   | a. 🖂 A check in the amount of \$860.00 to cover the above fees is enclosed   |  |   |                           |                                 |           |
|   | copy of this sheet is enclosed.  |  |   |                           |                                 |           |
| 1   | c.   The Commission  | er is hereby authori:  | zed to charge any add                                   | itional fees which may be | required or credit a            | ınv       |
| overpayment to Deposit Account No. 06-1050. A duplicate copy of this sheet is enclosed.   |  |  |   |                           | ,                               |           |
|   |  |  |   |                           | _                               |           |
| NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revi<br>(37 CFR 1.137(a) or (b) must be filed and granted to restore the application to pending status. |  |  |   |                           |                                 | e         |
| ١   | SEND ALL CORRESPONDENC   |  |   |                           | <b>3</b>                        |           |
| Γ   |  |  |   |                           | <i>c</i> •                      |           |
|   | Faustino A. Lichauco   | •  |   |                           | Mais-                           |           |
|   | FISH & RICHARDSON P.<br>225 Franklin Street  | U.   |   | SIGNATURE:                |                                 | 7.5.7     |
|   | Boston, MA 02110-2804  |  |   | NAME                      | Faustino A. L                   | ichauco   |
| (017) 542-5070 pnone  |  |  |   |                           |                                 |           |
| ı   | (617) 542-8906 facsimile   |  |   | REGISTRATION NUMBER       | 41,942                          | 2         |

20215196.doc

Attorney's Docket No.: 12816-008001 / S0751 SB/fis

JC08 Rec'd PCT/PTO 2 7 MAR 2001

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Heinrich Schenk Serial No.: Unassigned

Art Unit : Not yet assigned Examiner: Not yet assigned

Filed

: Herewith

Title

: DIGITAL RECEIVER FOR A SIGNAL PRODUCED USING DISCRETE

MULTITONE MODULATION

#### **Box PCT**

Commissioner for Patents Washington, D.C. 20231

#### PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

### In the specification:

Please replace the specification as filed with the attached substitute specification.

#### In the claims:

TĻ

Please amend claims 1-7 as shown in the attached claims.

#### In the abstract:

Please insert the attached abstract.

#### REMARKS

Applicant amends the claims to remove multiple dependencies and to more particularly and distinctly claim the subject matter of the invention. Applicant encloses a copy of the claims as pending, together with a copy showing differences between the claims as filed and the claims now pending.

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EL258935033US

I hereby certify under 37 CFR §1.10 that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

Signature

Typed or Printed Name of Person Signing Certificate

Applicant: Infineon Technologies AG

Attorney's Docket No.: 12816-008001 / S0751 SB/fis

Serial No. : Filed :

Page : 2

Pursuant to Rule 1.121(b)(3), Applicant requests that the attached substitute specification replace the originally filed specification. The attached substitute specification corrects minor typographical errors and adds section headers to facilitate examination of this application. In addition, references to elements in the drawings have been typeset in bold to enable the Examiner to more easily locate references to those elements in the specification. No new matter is introduced in this substitute specification. Applicant includes another version of the substitute specification marked to show revisions.

Applicant requests that the attached abstract replace that published in the PCT publication.

No additional fees are believed to be due in connection with the filing of this amendment. However, if additional fees are due, of if credits are forthcoming, please adjust our Deposit Account No. 06-1050.

Respectfully submitted,

Date: March 27, 2001

Faustino A. Lichauco Reg. No. 41,942

Fish & Richardson P.C. 225 Franklin Street Boston, MA 02110-2804 Telephone: (617) 542-5070 Facsimile: (617) 542-8906

20202952.doc

WO 00/19675

PCT/DE99/02752

JC08 Rec'd PCT/PTO 2 7 MAR 2001

Description

Digital receiver for a signal produced using discrete multitone modulation

5

The invention relates to a digital receiver for a signal produced using discrete multitone modulation, as claimed in the precharacterizing clause of patent claim 1.

10 Discrete multitone modulation (DMT) - also to multicarrier modulation - is as modulation method which is particularly suitable for channels transmitting data via in which distortion occurs. In comparison to so-called single-15 carrier methods such as amplitude modulation, which has one carrier frequency, discrete multitone modulation makes use of a large number of carrier frequencies. The amplitude and phase of each individual frequency is carrier modulated using quadrature amplitude modulation (QAM). This thus results in a 20 large number of QAM-modulated signals. A specific number of bits may in each case be transmitted per

used for digital audio broadcast DAB where it is referred to as OFDM (Orthogonal Frequency Division Multiplex) and for transmitting data via telephone lines, where it is referred to as ADSL (Asymmetric Digital Subscriber Line).

carrier frequency. Discrete multitone modulation is

In ADSL, a DMT-modulated signal is used to transmit data from a switching center via a subscriber line to a subscriber with an analog connection. In this case, ETSI and ANSI Standards state that each carrier frequency has a bandwidth of approximately 4 kHz, and that at most up to 15 bits per second per Hz are transported. The actual number of bits per second per Hz may differ for each carrier frequency, thus allowing the data rate and transmission spectrum to be matched to the transmission channel.

15

20

25

30

35

A DMT transmission system has a coder which combines the bits in a serial digital data signal which intended to be transmitted, to form blocks. A specific number of bits in a block in each case have an associated complex number. A complex number is used to represent a carrier frequency  $f_i = i/T$  where i = 1, 2, $\dots$ , N/2 in the discrete multitone modulation, with all the carrier frequencies  $f_{\rm i}$  being distributed at equal intervals. T is the time duration of a block. Inverse Fourier transformation is used to transform the carrier frequencies represented by the complex numbers to the time domain, where they directly represent N samples of a DMT signal to be transmitted. In order to allow Inverse Fast Fourier Transformation (IFFT) to be used. a power of two is selected for N. This reduces the complexity for Inverse Fast Fourier Transformation.

After the Inverse Fast Fourier Transformation, a cyclic prefix is carried out, with the last M (M < N)of the samples being attached once again to the start of a block. A periodic signal is thus simulated for a receiver, once the transient process produced by a transmission channel has decayed after M samples corresponding to а time  $T\cdot M/N$ . The equalization complexity in the receiver can be greatly reduced by means of the cyclic prefix since, after demodulation in the receiver, all that is necessary is multiplication by the inverse of the transfer function of the transmission channel in order to compensate for the linear distortion in the transmission channel. This requires one complex or four real multiplications for each carrier frequency.

In ADSL, the physical transmission channel is a two-wire line (twin-core copper cable) in the telephone network. The two-wire line requires a long time for the transient process in comparison to the length of a block. On the other hand, any additional transmission capacity required as a result of the cyclic prefix is intended to be as low as possible.

15

20

25

30

A cyclic prefix of M = 32 is defined in ADSL for a block length of N = 512. However, the transient process on the two-wire line has not yet decayed after M = 32 values. Additional errors thus occur in the receiver, which cannot be compensated for by a frequency-domain equalizer.

Such additional errors can be reduced by using special signal processing measures in the receiver.

To this end, a time domain equalizer (TDEQ) is connected upstream of a demodulator. The time domain equalizer is in the form of a digital transversal filter, whose coefficients are adjustable. The object the time domain equalizer is to shorten the transient process of the transmission channel. design of such time-domain equalizers is described in Al-Dhahir, N., Cioffi, J.M., "Optimum Finite-Length Equalization for Multicarrier Transceivers", Trans.on Comm., Vol. 44, No. 1, Jan 1996. However, this has the disadvantage that the digital transversal filter used as the time-domain equalizer has a large number of coefficients, and the adaptation of the digital transversal filter is complex. A filter length of 20 to 40 coefficients means that approximately 50 to 100 million multiplication operations must be carried out per second. In addition, each coefficient must be adjusted for adaptation of the digital transversal filter.

The technical problem on which the invention is based is thus to specify a digital receiver for a signal produced using discrete multitone modulation, which receiver has a time-domain equalizer which can be adapted more quickly and which carries our fewer multiplications per second.

This problem is solved by a digital receiver for a signal produced using discrete multitone modulation and having the features of patent claim 1. Advantageous refinements can be found in the respective dependent claims.

15

20

30

35

The invention relates to a digital receiver for a signal produced using discrete multitone modulation. The digital receiver has an analog/digital converter to which the signal produced using discrete multitone modulation is supplied, and has a time-domain equalizer connected downstream from the analog/digital converter. The time-domain equalizer in turn has a digital filter with fixed coefficients. The fixed coefficients of the digital filter as are required for adaptive digital filters and which require no effort for adaptation are advantageous in this case.

In one particularly preferred embodiment, the digital filter has integer values as fixed coefficients. It is particularly advantageous in this case that operations with integer values are less complex than operations with sliding-point values.

In a further particularly preferred embodiment, the digital filter has values which can be represented by shift operations as fixed coefficients. This advantageously allows multiplication operations to be replaced by shift operations, which are less complex.

In one preferred embodiment, the digital filter has a zero at 0 Hz, thus advantageously shortening the impulse response of the transmission system.

In a further preferred embodiment, the digital filter has a high-pass transfer function.

In one particularly preferred embodiment, the digital filter has a series circuit comprising a large number of first-order digital filters. The first-order filters can advantageously be produced very easily.

In a further particularly preferred embodiment, each first-order digital filter has a state memory, a shift register, a digital subtraction circuit and a digital addition circuit. The simple construction of each first-order filter is advantageous in this case, with no complex multiplication stages being required.

Further advantages, features and application options of the invention will become evident from the

following description of exemplary embodiments in conjunction with the drawing, in which:

Figure 1 shows a transmission path with a digital receiver for a signal produced using discrete multitone modulation; and

Figure 2 shows an exemplary embodiment of a timedomain equalizer according to the invention; and

Figure 3 shows a diagram illustrating the effect of a time-domain equalizer according to the invention.

15

20

25

10

In the transmission path illustrated in Figure 1 and having a digital receiver 12, a DMT transmitter 11 produces a signal modulated using discrete multitone modulation. The signal in this case has N/2 carrier frequencies f<sub>1</sub>, which are produced by discrete multitone modulation. Each carrier frequency is in this case amplitude-modulated and phase-modulated using quadrature amplitude modulation. In the DMT transmitter 11, the signal is provided with a cyclic prefix comprising M samples, and is converted digital/analog conversion to an analog signal transmission. The DMT transmitter 11 transmits the signal via a transmission channel 1 to the digital receiver 12.

The transmission channel 1 is a channel which produces linear distortion. In the case of ADSL transmission paths, the transmission channel is a two-wire line. Such linear distortion produced by the transmission channel 1 is compensated for once again in the digital receiver 12 by means of equalizers which operate in the frequency domain.

10

15

20

25

30

35

In the digital receiver 12, the signal is supplied to an analog/digital converter 2, which converts it to a sequence of digital values  $u_k$ .

The sequence of digital values  $u_k$  is supplied to a time-domain equalizer 3. The time-domain equalizer 3 is used to shorten the stabilization time of the DMT transmitter 11, of the transmission channel 1 and of time-domain equalizer 3 itself. If stabilization time is greater than the cyclic prefix time duration, errors occur in the decision-maker circuits 70 to 7n in the digital receiver 12. The timedomain equalizer 3 is intended to shorten the stabilization time without needing to produce any zeros in the frequency band which is used for transmission. To this end, the time-domain equalizer 3 has a digital filter with fixed coefficients and having the following transfer function  $(z = u_k)$ :

$$H(z) = \prod_{v=1}^{n} \left( \frac{1 - z^{-1}}{1 - c_v \cdot z^{-1}} \right) \text{ where } c_v = \pm (1 - 2^{-L})$$
 (1)

This is the transfer function of a multistage digital filter which has fixed coefficients  $c_{\nu}$  and is produced by a series circuit comprising n second first-order digital filters with a transfer function

$$H_v(z) = \frac{1-z^{-1}}{1-c_v \cdot z^{-1}}$$
 where  $c_v = \pm (1-2^{-L})$ . (2)

The transfer function H(z) of the time-domain equalizer 3 has a zero at 0 Hz, and is thus the transfer function of a high-pass filter. This is the most effective way to shorten the stabilization process of the transmission channel.

The digital values produced by the time-domain equalizer 3 are supplied to a serial/parallel converter 4 which removes the cyclic preface and produces blocks

25

which are supplied to a discrete Fast Fourier Transformation device 5.

The discrete Fast Fourier Transformation device 5 converts the signals represented by the blocks from the time domain to the frequency domain. Each converted block at the output of the discrete Fast Fourier Transformation device 5 has N/2 complex numbers. Each complex number represents a carrier frequency  $f_1 = i/T$  where  $i = 1, 2, \ldots, N/2$  for the discrete multitone modulation, with all the carrier frequencies  $f_1$  being distributed at equal intervals. T is the time duration of a block.

The discrete Fast Fourier Transformation device 5 is followed by a frequency-domain equalizer 60, ..., 6m for each carrier frequency  $f_1$ , ...,  $f_{N/2}$  and this carries out the equalization process in the frequency domain. To this end, each complex number in the conversion block which represents one carrier frequency is multiplied by the inverse transfer function of the transmission channel 1. This requires one complex multiplication operation, or four real multiplication operations.

Each frequency-domain equalizer 60, ..., 6m is followed by a respective decision-making circuit 70, ..., 7m, which produces a multistage value from the output signal from the frequency-domain equalizer 60, ..., 6m.

Each decision-making circuit 70, ..., 7m is in each case followed by a decoder circuit 80, ..., 8m, which produces a digital value from the multistage value.

The output signals from the decoder circuits 80, ..., 8m are supplied in parallel to a parallel/serial converter 9, which is connected to a data sink 10. The parallel/serial converter 9 supplies the data sink 10 with a serial stream of digital data, corresponding to the digital data from the DMT transmitter 11.

Figure 2 shows an exemplary embodiment of a time-domain equalizer according to the invention.

The time-domain equalizer has a series circuit comprising n second first-order digital filters with a transmission function as in Equation (2). Figure 2 shows only two first-order digital filters 100 and 200. Further second first-order digital filters are indicated by dots.

All the second first-order digital filters 100 10 and 200 are constructed in the same way. A discrete input value sequence is supplied to a first inverting input of a digital subtraction circuit 101 or 201, respectively, and, in parallel, to a first inverting input of a digital addition circuit 103 or 203, respectively. One output of the digital addition circuit 103 or 203, respectively, is an output of the second first-order digital filter and is fed back in parallel form to a non-inverting input of the digital subtraction circuit and, via a shift register, to a 20 second inverting input of the digital subtraction circuit 101 or 201, respectively. The shift register 104 or 204, respectively, multiplies a discrete output by shifting to the right, bit-by-bit. consequence, the discrete output value is multiplied by 25 integer number 2<sup>-L</sup>. One output of the digital subtraction circuit 101 or 201, respectively, is passed via a state memory 102 or 202, respectively, to a second non-inverting input of the digital addition circuit 103 or 203, respectively. The state memory 102 30 or 202, respectively, produces a delay by one clock period of the clock which is used to clock the discrete input sequence.

If L = 0 is chosen, the second digital filters 100 and 200 are non-recursive. In this case, in accordance with Equation (2), the coefficients  $c_{\rm v}$  become zero.

In one exemplary embodiment which is not illustrated, the second digital filters differ in the

integer number  $2^{-L}_v$  which is used to multiply a discrete output value from a second digital filter in the feedback path. In this exemplary embodiment, the coefficients  $c_v$  in accordance with Equation (1) differ for every alternate digital filter, and that digital filter which results from the series connection of the second digital filters has a transfer function in accordance with Equation (1).

Figure 3 uses two diagrams to illustrate the effect of six different exemplary embodiments of time-domain equalizers according to the invention. To this end, the signal-to-noise ratio and the input of the decision-making circuit was simulated and an ADSL transmission system having a two-wire line with a length of 3 km and a diameter of 0.4 mm.

Only the influences from the time-domain equalizer were considered. The signal-to-noise ratio is plotted over the entire frequency band used for ADSL transmission. A respective curve profile is indicated for each of the six different time-domain equalizers, whose respective transfer functions are  $H_1(z)$  to  $H_6(z)$ . The transfer functions  $H_1(z)$  to  $H_6(z)$  are as follows:

$$H_1(z) = 1 - z^{-1}$$

25

10

15

20

$$H_2(z) = (1-z^{-1})^2$$

$$H_3(z) = (1-z^{-1})^3$$

30 
$$H_4(z) = \left(\frac{1-z^{-1}}{1-0.5 \cdot z^{-1}}\right)$$

$$H_5(z) = \left(\frac{1 - z^{-1}}{1 - 0.5 \cdot z^{-1}}\right)^2$$

$$H_6(z) = \left(\frac{1 - z^{-1}}{1 - 0.5 \cdot z^{-1}}\right)^3$$

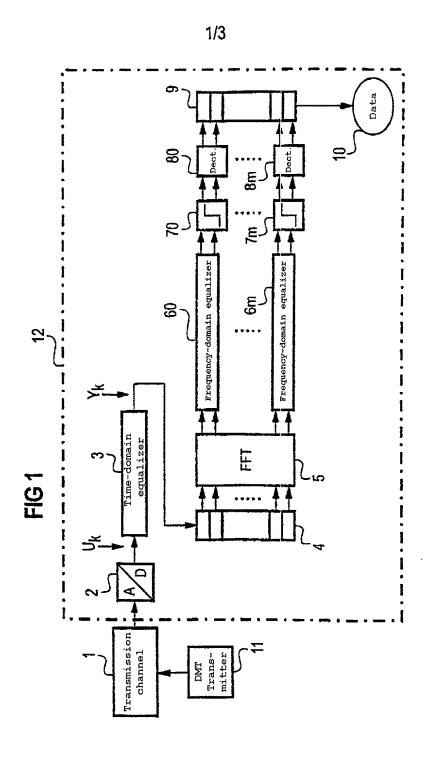
A curve profile without a time-domain equalizer and a curve profile having an optimized time-domain equalizer with 32 coefficients (32 taps) are shows for comparison. Both diagrams clearly show the improvement in the signal-to-noise ratio in the region of the lower frequencies. In the case of time-domain equalizers having a second, third or higher order digital filter, the signal-to-noise ratio differs from that of the optimized time-domain equalizer with 32 coefficients only by a few decibels above a frequency of about 300 kHz.

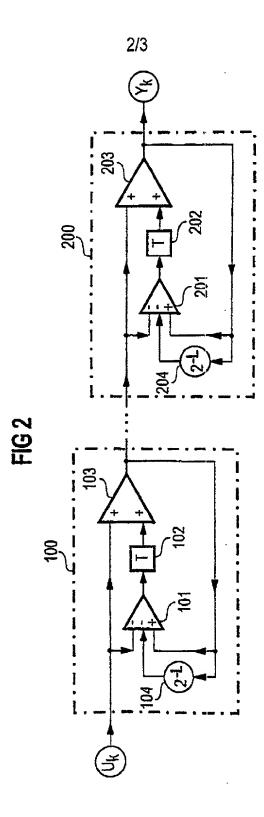
Patent Claims

- 1. A digital receiver for a signal (12) produced using discrete multitone modulation, which receiver has an analog/digital converter (2) to which the signal produced using discrete multitone modulation is supplied, and has a time-domain equalizer (3) connected downstream from the analog/digital converter, characterized in that
- the time-domain equalizer (3) has a digital filter with fixed coefficients (104, 204).
  - 2. The digital receiver as claimed in claim 1, characterized in that
- 15 the digital filter (100, 200) has integer values as fixed coefficients (104, 204).
  - 3. The digital receiver as claimed in claim 1 or 2,
- characterized in that the digital filter (100, 200) has values which can be represented by shift operations as fixed coefficients (104, 204).
- 4. The digital receiver as claimed in one of the preceding claims, characterized in that the digital filter (100, 200) has a zero at 0 Hz.
- 5. The digital receiver as claimed in one of the preceding claims, characterized in that the digital filter (100, 200) has a high-pass transfer function.
  - 6. The digital receiver as claimed in one of the preceding claims, characterized in that

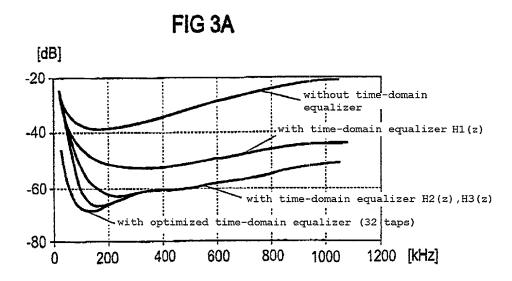
the digital filter has a series of circuits comprising a large number of first-order digital filters (100, 200).

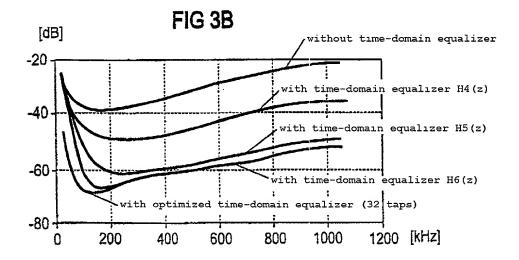
The digital receiver as claimed in claim 6, characterized in that each first-order digital filter has a state memory (102, 202), a shift register (104, 204), a digital subtraction circuit (101, 201) and a digital addition 10 circuit (103, 203).





3/3





20

25

Attorney Docket Number: 12816-008001

JC08 Rec'd PCT/PTO 2 7 MAR 2001

# DIGITAL RECEIVER FOR A SIGNAL PRODUCED USING DISCRETE MULTITONE MODULATION

#### FIELD OF INVENTION

The invention relates to a digital receiver for a signal produced using discrete multitone modulation

#### BACKGROUND

Discrete multitone modulation (DMT), also referred to as multicarrier modulation, is a modulation method which is particularly suitable for transmitting data via channels in which linear distortion occurs. In comparison to so-called single-carrier methods such as amplitude modulation, which has only one carrier frequency, discrete multitone modulation makes use of a large number of carrier frequencies. The amplitude and phase of each individual carrier frequency is modulated using quadrature amplitude modulation (QAM). This thus results in a large number of QAM-modulated signals. A specific number of bits may in each case be transmitted per carrier frequency. Discrete multitone modulation is used for digital audio broadcast DAB where it is referred to as OFDM (Orthogonal Frequency Division Multiplex) and for transmitting data via telephone lines, where it is referred to as ADSL (Asymmetric Digital Subscriber Line).

In ADSL, a DMT-modulated signal is used to transmit data from a switching center via a subscriber line to a subscriber with an analog connection. In this case, ETSI and ANSI Standards state that each carrier frequency has a bandwidth of approximately 4 kHz, and that at most up to 15 bits per second per Hz are transported. The actual number of bits per second per Hz may differ for each carrier frequency, thus allowing

Attorney Docket Number: 12816-008001 the data rate and transmission spectrum to be matched to the transmission channel.

A DMT transmission system has a coder which combines the bits in a serial digital data signal which is intended to be transmitted, to form blocks. A specific number of bits in a block in each case have an associated complex number. A complex number is used to represent a carrier frequency  $f_1 = i/T$  where i = 1,  $2, \ldots, N/2$  in the discrete multitone modulation, with all the carrier frequencies  $f_i$  being distributed at equal intervals. T is the time duration of a block. Inverse Fourier transformation is used to transform the carrier frequencies represented by the complex numbers to the time domain, where they directly represent  ${\tt N}$ samples of a DMT signal to be transmitted. In order to allow Inverse Fast Fourier Transformation (IFFT) to be used, a power of two is selected for N. This reduces the complexity for Inverse Fast Fourier Transformation.

After the Inverse Fast Fourier Transformation, a cyclic prefix is carried out, with the last M (M < N) 20 of the samples being attached once again to the start of a block. A periodic signal is thus simulated for a receiver, once the transient process produced by a transmission channel has decayed after M samples corresponding to a time T-M/N. The equalization 25 complexity in the receiver can be greatly reduced by means of the cyclic prefix since, after demodulation in the receiver, all that is necessary is multiplication by the inverse of the transfer function of the transmission channel in order to compensate for the linear distortion in the transmission channel. This requires one complex or four real multiplications for each carrier frequency.

In ADSL, the physical transmission channel is a two-wire line (twin-core copper cable) in the telephone network. The two-wire line requires a long time for the transient process in comparison to the length of a block. On the other hand, any additional transmission capacity required as a result of the cyclic prefix is intended to be as low as possible.

A cyclic prefix of M = 32 is defined in ADSL for a block length of N = 512. However, the transient process on the two-wire line has not yet decayed after M = 32 values. Additional errors thus occur in the receiver, which cannot be compensated for by a frequency-domain equalizer.

Such additional errors can be reduced by using special signal processing measures in the receiver.

To this end, a time domain equalizer (TDEQ) is connected upstream of a demodulator. The time domain equalizer is in the form of a digital transversal filter, whose coefficients are adjustable. The object of the time domain equalizer is to shorten the transient process of the transmission channel. The design of such time-domain equalizers is described in Al-Dhahir, N., Cioffi, J.M., "Optimum Finite-Length Equalization for Multicarrier Transceivers", IEEE Trans.on Comm., Vol. 44, No. 1, Jan 1996. However, this has the disadvantage that the digital transversal filter used as the time-domain equalizer has a large number of coefficients, and the adaptation of the digital transversal filter is complex. A filter length of 20 to 40 coefficients means that approximately 50 to 100 million multiplication operations must be carried out per second. In addition, each coefficient must be

Attorney Docket Number: 12816-008001 adjusted for adaptation of the digital transversal filter.

The technical problem on which the invention is based is thus to specify a digital receiver for a signal produced using discrete multitone modulation, which receiver has a time-domain equalizer which can be adapted more quickly and which carries out fewer multiplications per second.

#### SUMMARY

10

20

The invention relates to a digital receiver for a signal produced using discrete multitone modulation. The digital receiver has an analog/digital converter to which the signal produced using discrete multitone modulation is supplied, and has a time-domain equalizer connected downstream from the analog/digital converter. The time-domain equalizer in turn has a digital filter with fixed coefficients. The fixed coefficients of the digital filter as are required for adaptive digital filters and which require no effort for adaptation are advantageous in this case.

In one particularly preferred embodiment, the digital filter has integer values as fixed coefficients. It is particularly advantageous in this case that operations with integer values are less complex than operations with sliding-point values.

In a further particularly preferred embodiment, the digital filter has values which can be represented by shift operations as fixed coefficients. This advantageously allows multiplication operations to be replaced by shift operations, which are less complex.

Attorney Docket Number: 12816-008001

In one preferred embodiment, the digital filter has a zero at 0 Hz, thus advantageously shortening the impulse response of the transmission system.

In a further preferred embodiment, the digital filter has a high-pass transfer function.

In one particularly preferred embodiment, the digital filter has a series circuit comprising a large number of first-order digital filters. The first-order filters can advantageously be produced very easily.

In a further particularly preferred embodiment, each first-order digital filter has a state memory, a shift register, a digital subtraction circuit and a digital addition circuit. The simple construction of each first-order filter is advantageous in this case, with no complex multiplication stages being required.

Further advantages, features and application options of the invention will become evident from the following description of exemplary embodiments in conjunction with the drawing, in which:

#### 20 BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 shows a transmission path with a digital receiver for a signal produced using discrete multitone modulation;

Figure 2 shows an exemplary embodiment of a timedomain equalizer according to the invention; and

Figure 3 shows a diagram illustrating the effect of a time-domain equalizer according to the invention.

#### DETAILED DESCRIPTION

In the transmission path illustrated in Figure 1 and having a digital receiver 12, a DMT transmitter 11 produces a signal modulated using discrete multitone

5 modulation. The signal in this case has N/2 carrier frequencies f<sub>1</sub>, which are produced by discrete multitone modulation. Each carrier frequency is in this case amplitude-modulated and phase-modulated using quadrature amplitude modulation. In the DMT transmitter

10 11, the signal is provided with a cyclic prefix comprising M samples, and is converted by digital/analog conversion to an analog signal for transmission. The DMT transmitter 11 transmits the signal via a transmission channel 1 to the digital

15 receiver 12.

The transmission channel 1 is a channel which produces linear distortion. In the case of ADSL transmission paths, the transmission channel is a two-wire line. Such linear distortion produced by the transmission channel 1 is compensated for once again in the digital receiver 12 by means of equalizers which operate in the frequency domain.

In the digital receiver 12, the signal is supplied to an analog/digital converter 2, which converts it to a sequence of digital values  $u_k$ .

The sequence of digital values  $u_k$  is supplied to a time-domain equalizer  $\bf 3$ . The time-domain equalizer  $\bf 3$  is used to shorten the stabilization time of the DMT transmitter  $\bf 11$ , of the transmission channel  $\bf 1$  and of the time-domain equalizer  $\bf 3$  itself. If the stabilization time is greater than the cyclic prefix time duration, errors occur in the decision-maker

Attorney Docket Number: 12816-008001 circuits 70 to 7m in the digital receiver 12. The time-domain equalizer 3 is intended to shorten the stabilization time without needing to produce any zeros in the frequency band which is used for transmission. To this end, the time-domain equalizer 3 has a digital filter with fixed coefficients and having the following transfer function  $(z = u_k)$ :

$$H(z) = \prod_{v=1}^{n} \left( \frac{1 - z^{-1}}{1 - c_{v} \cdot z^{-1}} \right) \text{ where } c_{v} = \pm (1 - 2^{-L}) (1)$$

This is the transfer function of a multistage digital filter which has fixed coefficients  $c_{\rm v}$  and is produced by a series circuit comprising n second first-order digital filters with a transfer function

$$H_v(z) = \frac{1 - z^{-1}}{1 - c_v \cdot z^{-1}}$$
 where  $c_v = \pm (1 - 2^{-L})$  (2)

The transfer function H(z) of the time-domain equalizer 3 has a zero at 0 Hz, and is thus the transfer function of a high-pass filter. This is the most effective way to shorten the stabilization process of the transmission channel.

The digital values produced by the time-domain
equalizer 3 are supplied to a serial/parallel converter
which removes the cyclic preface and produces blocks
which are supplied to a discrete Fast Fourier
Transformation device 5.

The discrete Fast Fourier Transformation device 5 converts the signals represented by the blocks from the time domain to the frequency domain. Each converted block at the output of the discrete Fast Fourier Transformation device 5 has N/2 complex numbers. Each

Attorney Docket Number: 12816-008001 complex number represents a carrier frequency  $f_i = i/T$  where  $i=1,\,2,\ldots,\,N/2$  for the discrete multitone modulation, with all the carrier frequencies  $f_i$  being distributed at equal intervals. T is the time duration of a block.

The discrete Fast Fourier Transformation device  $\bf 5$  is followed by a frequency-domain equalizer  $\bf 60,\ldots, 6m$  for each carrier frequency  $f_1,\ldots, f_{N/2}$  and this carries out the equalization process in the frequency domain. To this end, each complex number in the conversion block which represents one carrier frequency is multiplied by the inverse transfer function of the transmission channel  $\bf 1$ . This requires one complex multiplication operation, or four real multiplication operations.

Each frequency-domain equalizer 60,..., 6m is followed by a respective decision-making circuit 70,..., 7m, which produces a multistage value from the output signal from the frequency-domain equalizer 60,..., 6m.

Each decision-making circuit  $70, \ldots, 7m$  is in each case followed by a decoder circuit  $80, \ldots, 8m$ , which produces a digital value from the multistage value.

The output signals from the decoder circuits

80,..., 8m are supplied in parallel to a
parallel/serial converter 9, which is connected to a
data sink 10. The parallel/serial converter 9 supplies
the data sink 10 with a serial stream of digital data,
corresponding to the digital data from the DMT
transmitter 11.

Attorney Docket Number: 12816-008001

Figure 2 shows an exemplary embodiment of a time-domain equalizer according to the invention.

The time-domain equalizer has a series circuit comprising n second first-order digital filters with a transmission function as in Equation (2). Figure 2 shows only two first-order digital filters 100 and 200. Further second first-order digital filters are indicated by dots.

All the second first-order digital filters 100 and 200 are constructed in the same way. A discrete input value sequence is supplied to a first inverting input of a digital subtraction circuit 101 or 201, respectively, and, in parallel, to a first noninverting input of a digital addition circuit 103 or 203, respectively. One output of the digital addition circuit 103 or 203, respectively, is an output of the second first-order digital filter and is fed back in parallel form to a non-inverting input of the digital subtraction circuit and, via a shift register, to a second inverting input of the digital subtraction circuit 101 or 201, respectively. The shift register 104 or 204, respectively, multiplies a discrete output value by shifting to the right, bit-by-bit. In consequence, the discrete output value is multiplied by an integer number 2<sup>-L</sup>. One output of the digital subtraction circuit 101 or 201, respectively, is passed via a state memory 102 or 202, respectively, to a second non-inverting input of the digital addition circuit 103 or 203, respectively. The state memory 102 or 202, respectively, produces a delay by one clock period of the clock which is used to clock the discrete input sequence.

20

Attorney Docket Number: 12816-008001

If L = 0 is chosen, the second digital filters 100 and 200 are non-recursive. In this case, in accordance with Equation (2), the coefficients  $c_{\nu}$  become zero.

In one exemplary embodiment which is not illustrated, the second digital filters differ in the integer number  $2^{-L}_{v}$  which is used to multiply a discrete output value from a second digital filter in the feedback path. In this exemplary embodiment, the coefficients  $c_{v}$  in accordance with Equation (1) differ for every alternate digital filter, and that digital filter which results from the series connection of the second digital filters has a transfer function in accordance with Equation (1).

Figure 3 uses two diagrams to illustrate the effect of six different exemplary embodiments of time-domain equalizers according to the invention. To this end, the signal-to-noise ratio and the input of the decision-making circuit was simulated on an ADSL transmission system having a two-wire line with a length of 3 km and a diameter of 0.4 mm.

Only the influences from the time-domain equalizer were considered. The signal-to-noise ratio is plotted over the entire frequency band used for ADSL transmission. A respective curve profile is indicated for each of the six different time-domain equalizers, whose respective transfer functions are  $H_1(z)$  to  $H_6(z)$ . The transfer functions  $H_1(z)$  to  $H_6(z)$  are as follows:

$$H_1(z) = 1 - z^{-1}$$

$$H_2(z) = (1-z^{-1})^2$$

30 
$$H_3(z) = (1-z^{-1})^3$$

$$H_4(z) = \left(\frac{1-z^{-1}}{1-0.5 \cdot z^{-1}}\right)$$

$$H_5(z) = \left(\frac{1 - z^{-1}}{1 - 0.5 \cdot z^{-1}}\right)^2$$

$$H_6(z) = \left(\frac{1 - z^{-1}}{1 - 0.5 \cdot z^{-1}}\right)^3$$

A curve profile without a time-domain equalizer and a curve profile having an optimized time-domain equalizer with 32 coefficients (32 taps) are shown for comparison. Both diagrams clearly show the improvement in the signal-to-noise ratio in the region of the lower frequencies. In the case of time-domain equalizers having a second, third or higher order digital filter, the signal-to-noise ratio differs from that of the optimized time-domain equalizer with 32 coefficients only by a few decibels above a frequency of about 300 kHz.

Having described the invention, and a preferred embodiment thereof, what is claimed as new, and secure by letters patent is:

#### Attorney Docket Number: 12816-008001

#### CLAIMS PENDING AS OF MARCH 27, 2001

- 1. A digital receiver for receiving an input signal produced using discrete multitone modulation, said receiver comprising:
  - an analog/digital converter to which the input signal is supplied, and
  - a time-domain equalizer connected downstream from the analog/digital converter, the time-domain equalizer including a digital filter having fixed coefficients.
  - The digital receiver as claimed in claim 1, wherein the fixed coefficients of the digital filter have integer values.
- The digital receiver as claimed in claim 1, wherein the fixed coefficients of the digital filter have values that can be represented by shift operations.
- 4. The digital receiver as claimed in claim 1, wherein the digital filter has a zero at 0 Hz.
  - 5. The digital receiver as claimed in claim 1, wherein the digital filter is a high-pass filter.
- 6. The digital receiver as claimed in claim 1, wherein the digital filter comprises a series of circuits, each of the circuits having a plurality of first-order digital filters.
  - 7. The digital receiver as claimed in claim 6, wherein each first-order digital filter comprises:
    - a state memory,

- a shift register,
- a subtraction circuit, and
- an addition circuit.

#### ABSTRACT

A digital receiver for receiving DMT signals includes a time-domain equalizer that includes a digital filter having fixed coefficients.

20209036.doc

# COPY OF SPECIFICATION SHOWING DIFFERENCES BETWEEN SUBSTITUTE SPECIFICATION AND SPECIFICATION AS FILED

Description

5

25

# DIGITAL RECEIVER FOR A SIGNAL PRODUCED USING DISCRETE MULTITONE MODULATION

#### FIELD OF INVENTION

The invention relates to a digital receiver for a signal produced using discrete multitone modulation, as claimed in the precharacterizing clause of patent claim 1.

#### BACKGROUND

Discrete multitone modulation (DMT),— also referred to as multicarrier modulation, - is a modulation method which is particularly suitable for transmitting data via channels in which linear distortion occurs. In comparison to so-called single-carrier methods such as amplitude modulation, which has only one carrier frequency, discrete multitone modulation makes use of a large number of carrier frequencies. The amplitude and phase of each individual carrier frequency is modulated using quadrature amplitude modulation (QAM). This thus results in a large number of QAM-modulated signals. A specific number of bits may in each case be transmitted per carrier frequency. Discrete multitone modulation is used for digital audio broadcast DAB where it is referred to as OFDM (Orthogonal Frequency Division Multiplex) and for transmitting data via telephone lines, where it is referred to as ADSL (Asymmetric Digital Subscriber Line).

In ADSL, a DMT-modulated signal is used to transmit data from a switching center via a subscriber line to a subscriber with an analog connection. In this case, ETSI and ANSI Standards state that each carrier frequency has a bandwidth of approximately 4 kHz, and that at most up to 15 bits per second per Hz are transported. The actual number of bits per second per Hz may differ for each carrier frequency, thus allowing the

30

Attorney Docket Number: 12816-008001

data rate and transmission spectrum to be matched to the transmission channel.

A DMT transmission system has a coder which combines the bits in a serial digital data signal which is intended to be transmitted, to form blocks. A specific number of bits in a block in each case have an associated complex number. A complex number is used to represent a carrier frequency  $f_i = i/T$  where  $i = 1, 2, \ldots, N/2$  in the discrete multitone modulation, with all the carrier frequencies  $f_i$  being distributed at equal intervals. T is the time duration of a block. Inverse Fourier transformation is used to transform the carrier frequencies represented by the complex numbers to the time domain, where they directly represent N samples of a DMT signal to be transmitted. In order to allow Inverse Fast Fourier Transformation (IFFT) to be used, a power of two is selected for N. This reduces the complexity for Inverse Fast Fourier Transformation.

After the Inverse Fast Fourier Transformation, a cyclic prefix is carried out, with the last M (M < N) of the samples being attached once again to the start of a block. A periodic signal is thus simulated for a receiver, once the transient process produced by a transmission channel has decayed after M samples corresponding to a time  $T \cdot M/N$ . The equalization complexity in the receiver can be greatly reduced by means of the cyclic prefix since, after demodulation in the receiver, all that is necessary is multiplication by the inverse of the transfer function of the transmission channel in order to compensate for the linear distortion in the transmission channel. This requires one complex or four real multiplications for each carrier frequency.

In ADSL, the physical transmission channel is a two-wire line (twin-core copper cable) in the telephone network. The two-wire line requires a long time for the transient process in comparison to the length of a block. On the other hand, any

25

Attorney Docket Number: 12816-008001

additional transmission capacity required as a result of the cyclic prefix is intended to be as low as possible.

A cyclic prefix of M=32 is defined in ADSL for a block length of N=512. However, the transient process on the two-wire line has not yet decayed after M=32 values. Additional errors thus occur in the receiver, which cannot be compensated for by a frequency-domain equalizer.

Such additional errors can be reduced by using special signal processing measures in the receiver.

To this end, a time domain equalizer (TDEQ) is connected upstream of a demodulator. The time domain equalizer is in the form of a digital transversal filter, whose coefficients are adjustable. The object of the time domain equalizer is to shorten the transient process of the transmission channel. The design of such time-domain equalizers is described in Al-Dhahir, N., Cioffi, J.M., "Optimum Finite-Length Equalization for Multicarrier Transceivers", IEEE Trans.on Comm., Vol. 44, No. 1, Jan 1996. However, this has the disadvantage that the digital transversal filter used as the time-domain equalizer has a large number of coefficients, and the adaptation of the digital transversal filter is complex. A filter length of 20 to 40 coefficients means that approximately 50 to 100 million multiplication operations must be carried out per second. In addition, each coefficient must be adjusted for adaptation of the digital transversal filter.

The technical problem on which the invention is based is thus to specify a digital receiver for a signal produced using discrete multitone modulation, which receiver has a time-domain equalizer which can be adapted more quickly and which carries outr fewer multiplications per second.

#### SUMMARY

This problem is solved by a digital receiver for a signal produced using discrete multitone modulation and having the

Attorney Docket Number: 12816-008001

features of patent claim 1. Advantageous refinements can be found in the respective dependent claims.

The invention relates to a digital receiver for a signal produced using discrete multitone modulation. The digital receiver has an analog/digital converter to which the signal produced using discrete multitone modulation is supplied, and has a time-domain equalizer connected downstream from the analog/digital converter. The time-domain equalizer in turn has a digital filter with fixed coefficients. The fixed coefficients of the digital filter as are required for adaptive digital filters and which require no effort for adaptation are advantageous in this case.

In one particularly preferred embodiment, the digital filter has integer values as fixed coefficients. It is particularly advantageous in this case that operations with integer values are less complex than operations with sliding-point values.

In a further particularly preferred embodiment, the digital filter has values which can be represented by shift operations as fixed coefficients. This advantageously allows multiplication operations to be replaced by shift operations, which are less complex.

In one preferred embodiment, the digital filter has a zero at 0 Hz, thus advantageously shortening the impulse response of the transmission system.

In a further preferred embodiment, the digital filter has a high-pass transfer function.

In one particularly preferred embodiment, the digital filter has a series circuit comprising a large number of first-order digital filters. The first-order filters can advantageously be produced very easily.

15

20

Attorney Docket Number: 12816-008001

In a further particularly preferred embodiment, each first-order digital filter has a state memory, a shift register, a digital subtraction circuit and a digital addition circuit. The simple construction of each first-order filter is advantageous in this case, with no complex multiplication stages being required.

Further advantages, features and application options of the invention will become evident from the following description of exemplary embodiments in conjunction with the drawing, in which:

### BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 shows a transmission path with a digital receiver for a signal produced using discrete multitone modulation; and

Figure 2 shows an exemplary embodiment of a time-domain equalizer according to the invention; and

Figure 3 shows a diagram illustrating the effect of a time-domain equalizer according to the invention.

#### DETAILED DESCRIPTION

In the transmission path illustrated in Figure 1 and having a digital receiver 12, a DMT transmitter 11 produces a signal modulated using discrete multitone modulation. The signal in this case has N/2 carrier frequencies  $f_1$ , which are produced by discrete multitone modulation. Each carrier frequency is in this case amplitude-modulated and phase-modulated using quadrature amplitude modulation. In the DMT transmitter 11, the signal is provided with a cyclic prefix comprising M samples, and is converted by digital/analog conversion to an analog signal for transmission. The DMT transmitter 11 transmits the signal via a transmission channel 1 to the digital receiver 12.

15

20

25

Attorney Docket Number: 12816-008001

The transmission channel 1 is a channel which produces linear distortion. In the case of ADSL transmission paths, the transmission channel is a two-wire line. Such linear distortion produced by the transmission channel 1 is compensated for once again in the digital receiver 12 by means of equalizers which operate in the frequency domain.

In the digital receiver  ${\bf 12}$ , the signal is supplied to an analog/digital converter  ${\bf 2}$ , which converts it to a sequence of digital values  $u_k$ .

The sequence of digital values  $u_k$  is supplied to a time-domain equalizer 3. The time-domain equalizer 3 is used to shorten the stabilization time of the DMT transmitter 11, of the transmission channel 1 and of the time-domain equalizer 3 itself. If the stabilization time is greater than the cyclic prefix time duration, errors occur in the decision-maker circuits 70 to  $7\pi m$  in the digital receiver 12. The time-domain equalizer 3 is intended to shorten the stabilization time without needing to produce any zeros in the frequency band which is used for transmission. To this end, the time-domain equalizer 3 has a digital filter with fixed coefficients and having the following transfer function  $(z = u_k)$ :

$$H(z) = \prod_{v=1}^{n} \left( \frac{1-z^{-1}}{1-c_{v} \cdot z^{-1}} \right) \text{ where } c_{v} = \pm (1-2^{-L}) (1)$$

This is the transfer function of a multistage digital filter which has fixed coefficients  $c_{\nu}$  and is produced by a series circuit comprising n second first-order digital filters with a transfer function

$$H_v(z) = \frac{1 - z^{-1}}{1 - c_v \cdot z^{-1}}$$
 where  $c_v = \pm (1 - 2^{-L}) + (2)$ 

The transfer function H(z) of the time-domain equalizer  ${\bf 3}$  has a zero at 0 Hz, and is thus the transfer function of a

15

20

Attorney Docket Number: 12816-008001

high-pass filter. This is the most effective way to shorten the stabilization process of the transmission channel.

The digital values produced by the time-domain equalizer 3 are supplied to a serial/parallel converter 4 which removes the cyclic preface and produces blocks which are supplied to a discrete Fast Fourier Transformation device 5.

The discrete Fast Fourier Transformation device  $\bf 5$  converts the signals represented by the blocks from the time domain to the frequency domain. Each converted block at the output of the discrete Fast Fourier Transformation device  $\bf 5$  has N/2 complex numbers. Each complex number represents a carrier frequency  $f_{i1}$  = i/T where i = 1, 2, ..., N/2 for the discrete multitone modulation, with all the carrier frequencies  $f_{i1}$  being distributed at equal intervals. T is the time duration of a block.

The discrete Fast Fourier Transformation device  $\mathbf{5}$  is followed by a frequency-domain equalizer  $\mathbf{60}$ , ...,  $\mathbf{6m}$  for each carrier frequency  $f_1$ , ...,  $f_{\mathbb{N}/2}$  and this carries out the equalization process in the frequency domain. To this end, each complex number in the conversion block which represents one carrier frequency is multiplied by the inverse transfer function of the transmission channel  $\mathbf{1}$ . This requires one complex multiplication operation, or four real multiplication operations.

Each frequency-domain equalizer 60, ..., 6m is followed by a respective decision-making circuit 70, ..., 7m, which produces a multistage value from the output signal from the frequency-domain equalizer 60, ..., 6m.

Each decision-making circuit 70, ..., 7m is in each case followed by a decoder circuit 80, ..., 8m, which produces a digital value from the multistage value.

15

20

25

#### Attorney Docket Number: 12816-008001

The output signals from the decoder circuits 80, ..., 8m are supplied in parallel to a parallel/serial converter 9, which is connected to a data sink 10. The parallel/serial converter 9 supplies the data sink 10 with a serial stream of digital data, corresponding to the digital data from the DMT transmitter 11.

Figure 2 shows an exemplary embodiment of a time-domain equalizer according to the invention.

The time-domain equalizer has a series circuit comprising n second first-order digital filters with a transmission function as in Equation (2). Figure 2 shows only two first-order digital filters 100 and 200. Further second first-order digital filters are indicated by dots.

All the second first-order digital filters 100 and 200 are constructed in the same way. A discrete input value sequence is supplied to a first inverting input of a digital subtraction circuit 101 or 201, respectively, and, in parallel, to a first non-inverting input of a digital addition circuit 103 or 203, respectively. One output of the digital addition circuit 103 or 203, respectively, is an output of the second first-order digital filter and is fed back in parallel form to a noninverting input of the digital subtraction circuit and, via a shift register, to a second inverting input of the digital subtraction circuit 101 or 201, respectively. The shift register 104 or 204, respectively, multiplies a discrete output value by shifting to the right, bit-by-bit. In consequence, the discrete output value is multiplied by an integer number 2<sup>-L</sup>. One output of the digital subtraction circuit 101 or 201, respectively, is passed via a state memory 102 or 202, respectively, to a second non-inverting input of the digital addition circuit 103 or 203, respectively. The state memory 102 or 202, respectively, produces a delay by one clock period of the clock which is used to clock the discrete input sequence.

25

If L=0 is chosen, the second digital filters 100 and 200 are non-recursive. In this case, in accordance with Equation (2), the coefficients  $c_v$  become zero.

In one exemplary embodiment which is not illustrated, the second digital filters differ in the integer number  $2^{-L}_{v}$  which is used to multiply a discrete output value from a second digital filter in the feedback path. In this exemplary embodiment, the coefficients  $c_{v}$  in accordance with Equation (1) differ for every alternate digital filter, and that digital filter which results from the series connection of the second digital filters has a transfer function in accordance with Equation (1).

Figure 3 uses two diagrams to illustrate the effect of six different exemplary embodiments of time-domain equalizers according to the invention. To this end, the signal-to-noise ratio and the input of the decision-making circuit was simulated andon an ADSL transmission system having a two-wire line with a length of 3 km and a diameter of 0.4 mm.

Only the influences from the time-domain equalizer were considered. The signal-to-noise ratio is plotted over the entire frequency band used for ADSL transmission. A respective curve profile is indicated for each of the six different time-domain equalizers, whose respective transfer functions are  $H_1(z)$  to  $H_6(z)$ . The transfer functions  $H_1(z)$  to  $H_6(z)$  are as follows:

$$H_1(z) = 1 - z^{-1}$$

$$H_2(z) = (1-z^{-1})^2$$

$$H_3(z) = (1-z^{-1})^3$$

$$H_4(z) = \left(\frac{1-z^{-1}}{1-0.5 \cdot z^{-1}}\right)$$

$$H_5(z) = \left(\frac{1-z^{-1}}{1-0.5 \cdot z^{-1}}\right)^2$$

$$H_6(z) = \left(\frac{1 - z^{-1}}{1 - 0.5 \cdot z^{-1}}\right)^3$$

A curve profile without a time-domain equalizer and a curve profile having an optimized time-domain equalizer with 32 coefficients (32 taps) are showns for comparison. Both diagrams clearly show the improvement in the signal-to-noise ratio in the region of the lower frequencies. In the case of time-domain equalizers having a second, third or higher order digital filter, the signal-to-noise ratio differs from that of the optimized time-domain equalizer with 32 coefficients only by a few decibels above a frequency of about 300 kHz.

Having described the invention, and a preferred embodiment thereof, what is claimed as new, and secure by letters patent is:

10

15

20

30

# DIFFERENCES BETWEEN CLAIMS PENDING AND CLAIMS ORIGINALLY FILED

- 1. A digital receiver for <u>receiving an input</u> signal (12) produced using discrete multitone modulation, which <u>said</u> receiver comprising:
  - has—an analog/digital converter (2) to which the <u>input</u> signal—produced using discrete multitone modulation is supplied, and has
- a time-domain equalizer (3) connected downstream from the
  analog/digital converter, characterized in that the
  time-domain equalizer (3) includinghas a digital filter
  withhaving fixed coefficients (104, 204).
  - 2. The digital receiver as claimed in claim 1, characterized in that wherein the fixed coefficients of the digital filter (100, 200) has have integer values as fixed coefficients (104, 204).
  - 3. The digital receiver as claimed in claim 1 or 2, characterized in that wherein the fixed coefficients of the digital filter (100, 200) has have values that which can be represented by shift operations as fixed coefficients (104, 204).
  - 4. The digital receiver as claimed in one of the preceding claims 1, whereincharacterized in that the digital filter (100, 200) has a zero at 0 Hz.
- 5. The digital receiver as claimed in one of the preceding claims 1, characterized in that wherein the digital filter (100, 200) ishas a high-pass transfer function filter.
  - 6. The digital receiver as claimed in one of the preceding claims 1, whereincharacterized in that the digital filter comprises has a series of circuits, each of the circuits

Attorney Docket Number: 12816-008001

having comprising a pluralitylarge number of first-order digital filters—(100, 200).

- 7. The digital receiver as claimed in claim 6, characterized in that wherein each first-order digital filter has comprises:
  - a state memory (102, 202),
  - a shift register <del>(104, 204)</del>,
  - a digital subtraction circuit, (101, 201) and
  - an digital addition circuit (103, 203).



Attorney's Docket No.: 12816-008001 Client's Ref. No.: S0751 SB/fis

#### COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

is attached hereto.

[]

My residence, post office address and citizenship are as stated below next to my name.

[X] was described and claimed in PCT International Application No. <u>DE/99/02752</u>
September 1, 1999 and as amended under PCT Article 19 on \_\_\_\_\_

was filed on \_ as Application Serial No. \_ and was amended on \_

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled <u>DIGITAL RECEIVER FOR A SIGNAL PRODUCED USING DISCRETE</u> <u>MULTITONE MODULATION</u>, the specification of which:

| I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.  |  |  |   |                        |  |
|--|--|--|---|------------------------|--|
| Title 37   | I acknowledge the duty to disclose all information I know to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56. |  |   |                        |  |
| I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed: |  |  |   |                        |  |
| Country Application No.  |  | Filing Date  | Priority Claimed  |                        |  |
| C  | Jermany  | 198 44 460.5   | September 28, 1998  | [X] Yes [] No          |  |
|  |  | 170 17 100.5   | September 28, 1998  | [X] Ies [] No          |  |
| business   | I hereby appoint the   |  | gents to prosecute this applicatio  |                        |  |
|  | I hereby appoint the   | e following attorneys and/or a rademark Office connected the | gents to prosecute this applicatio  | on and to transact all |  |
|  | I hereby appoint the s in the Patent and To  | e following attorneys and/or a rademark Office connected th  | ngents to prosecute this application erewith:   | on and to transact all |  |
|  | I hereby appoint the s in the Patent and To Cocchiuti, Reg. No. Address all telepho  | e following attorneys and/or a rademark Office connected th  | agents to prosecute this application erewith:  Faustino A. Lichauco, Reg. No CHAUCO at telephone number ( | on and to transact all |  |

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Attorney's Docket No.: 12816-008001 Client's Ref. No.: S0751 SB/fis

## **Combined Declaration and Power of Attorney**

Page 2 of 2 Pages

| 1  | , | 0 | 0 |
|----|---|---|---|
| ١. | _ | - |   |

| J | ŭ.                     | •                     |
|---|------------------------|-----------------------|
|   | Full Name of Inventor: | HEINRICH SCHENK       |
|   | Inventor's Signature:  | Heinrich Clas Date:   |
|   | Residence Address:     | Fatimastr. 3          |
|   |                        | <u>Munich D-81476</u> |
|   |                        | Germany               |
|   | Citizenship:           | Germany               |
|   | Post Office Address:   | Fatimastr. 3          |
|   |                        | Munich D-81476        |

20215097 doc

Germany